

1/14

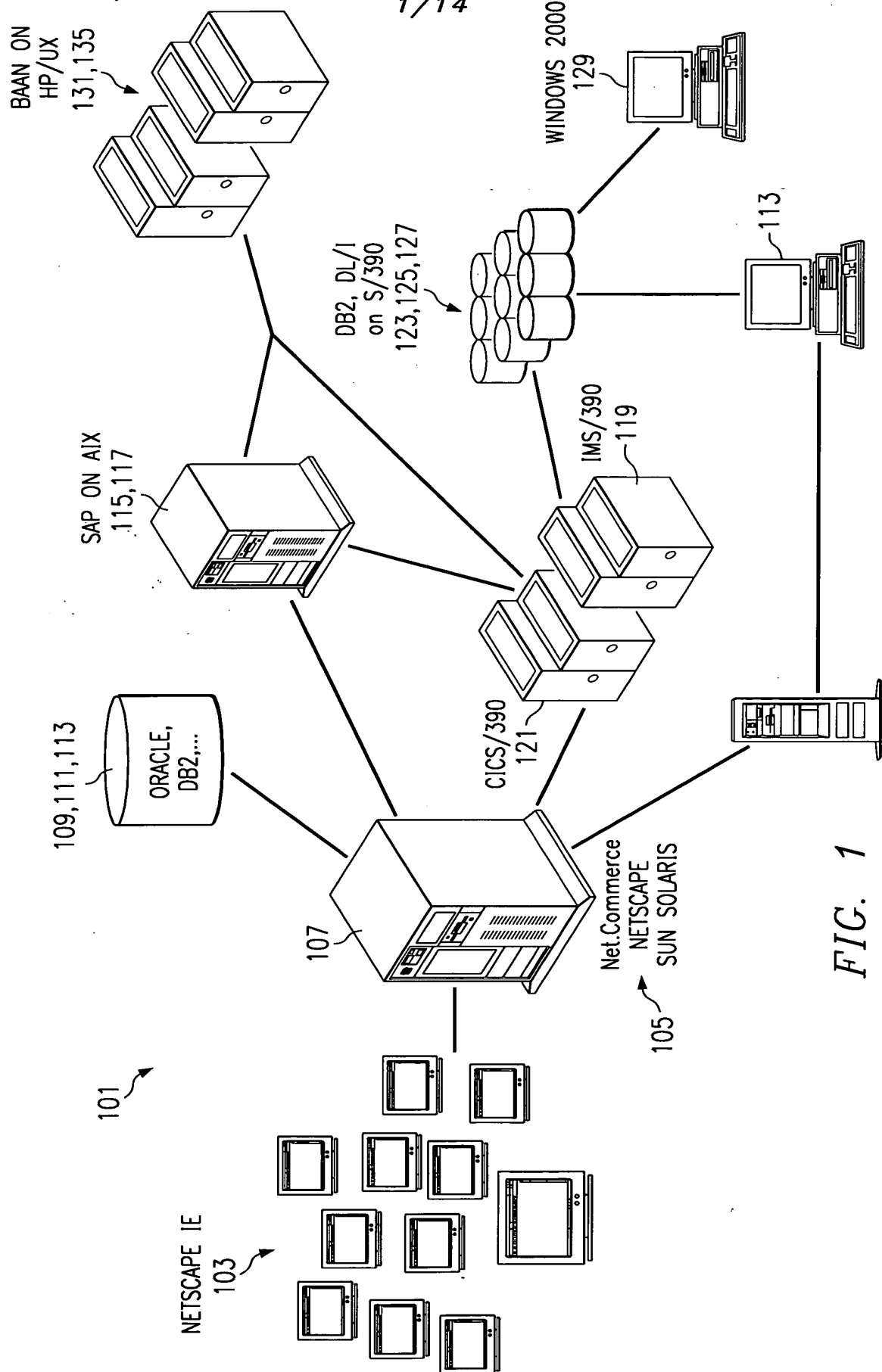


FIG. 1

2/14

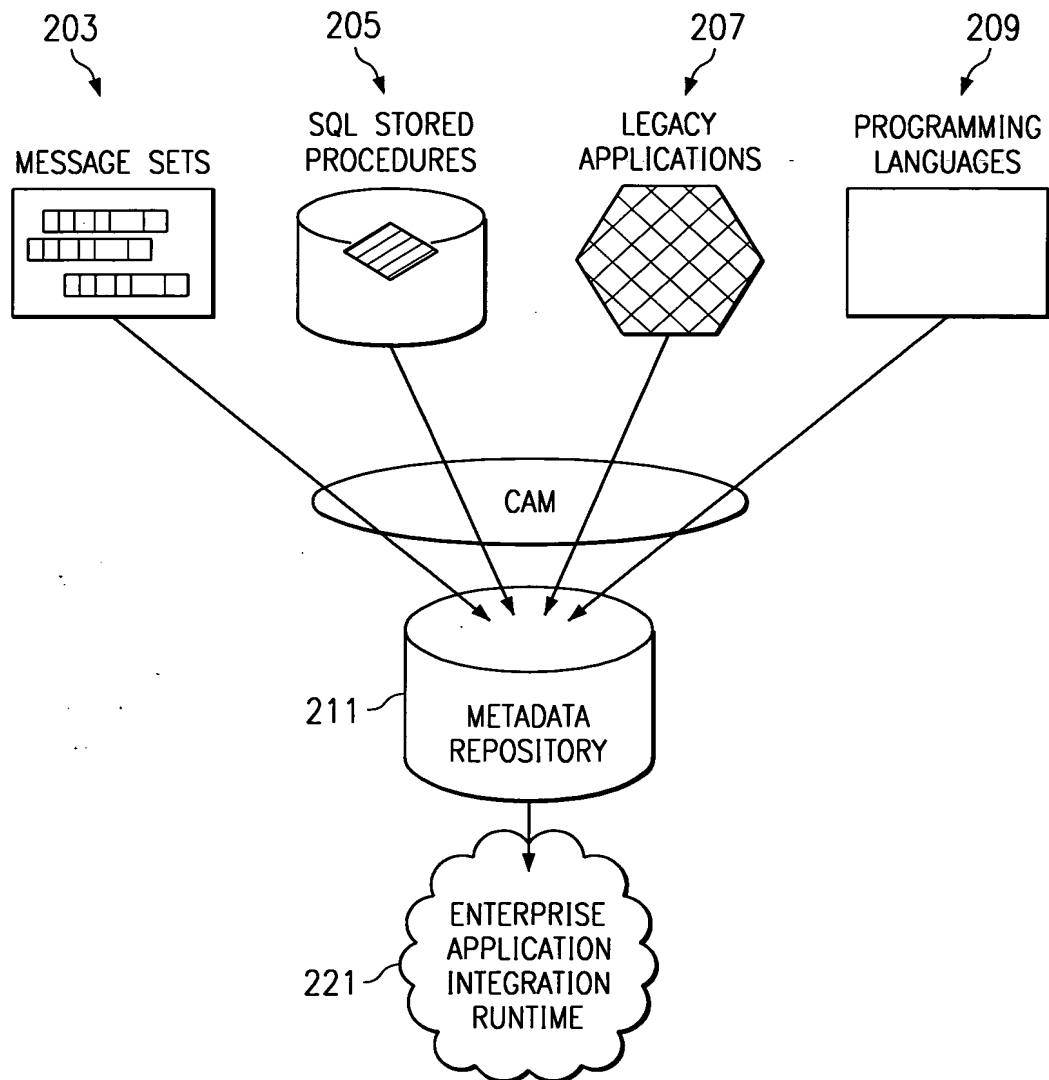


FIG. 2

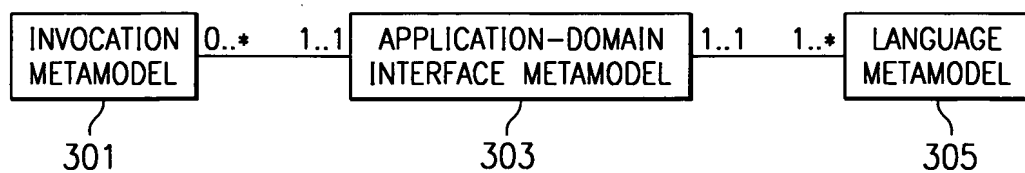
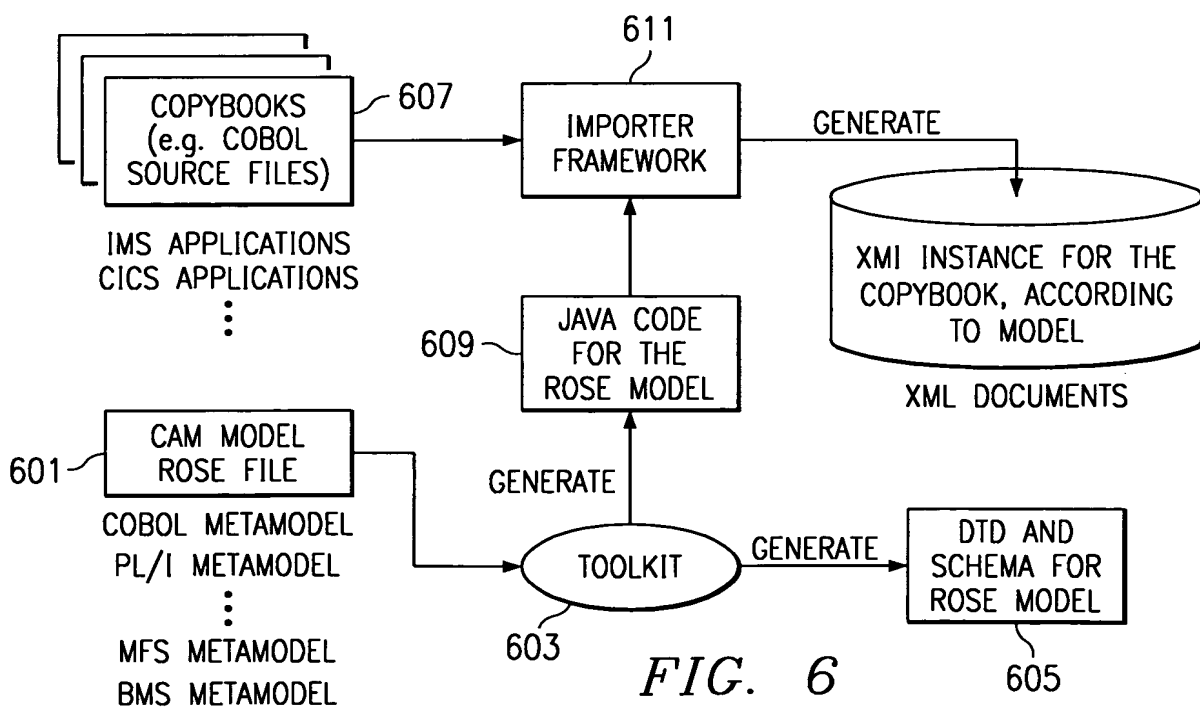
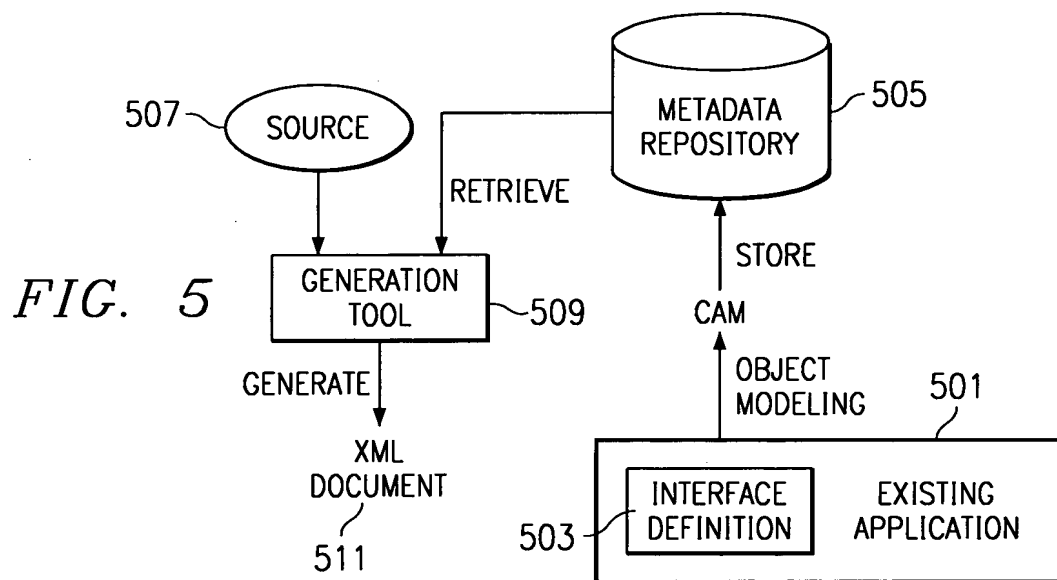
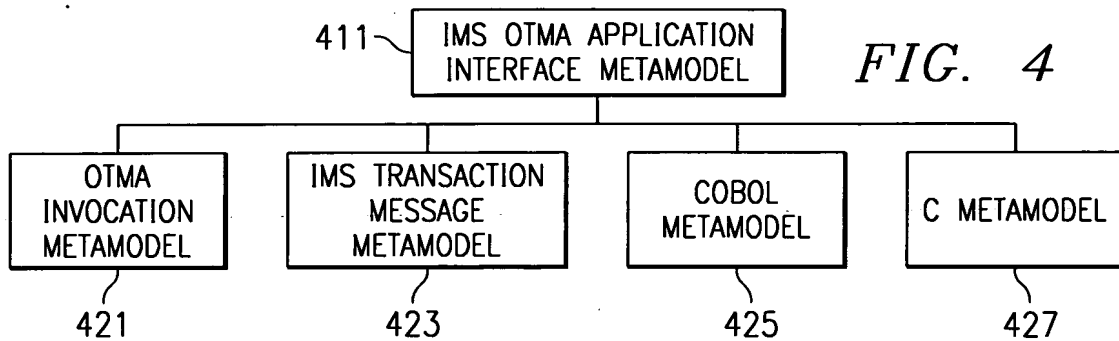


FIG. 3

3/14



4/14

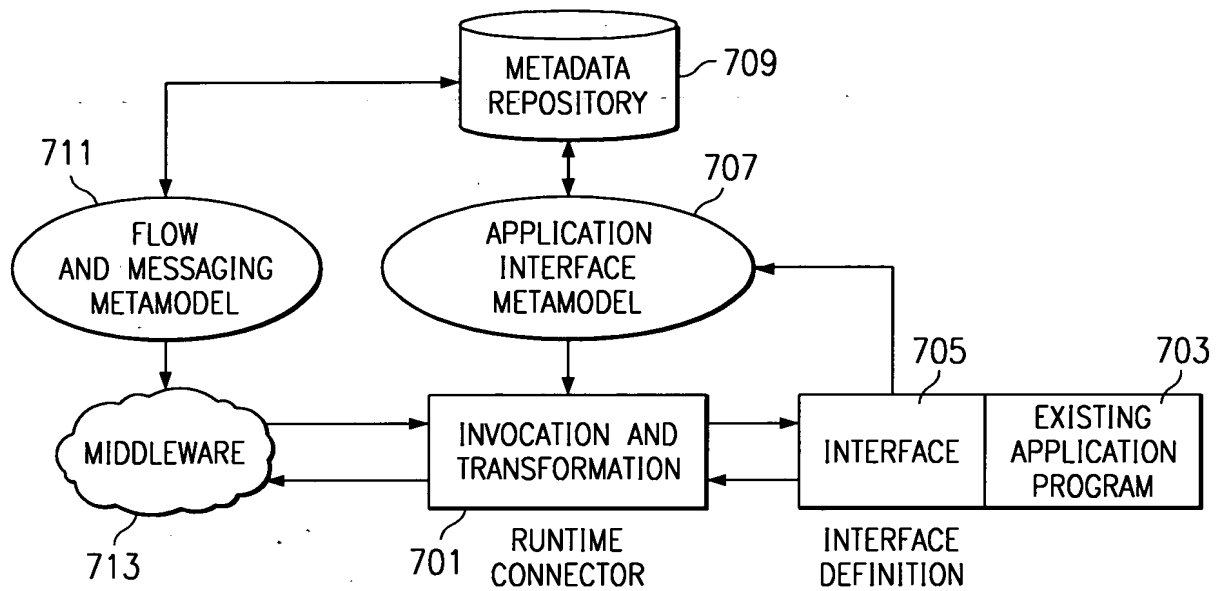


FIG. 7

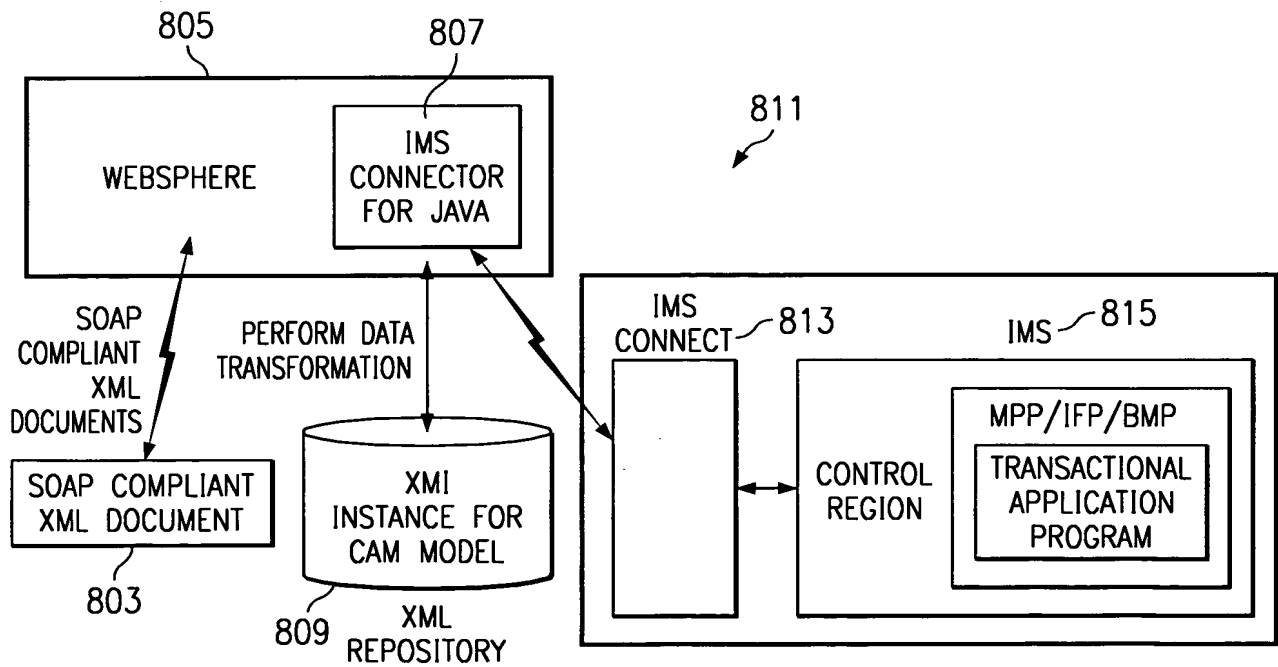


FIG. 8

5/14

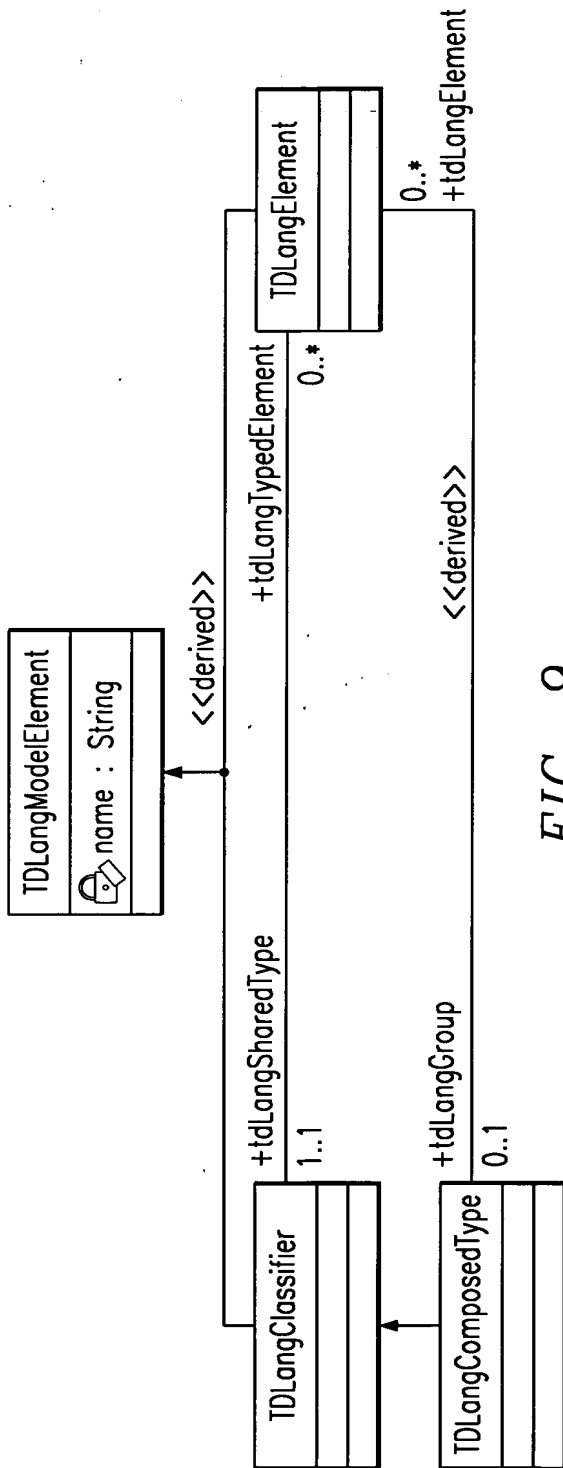


FIG. 9

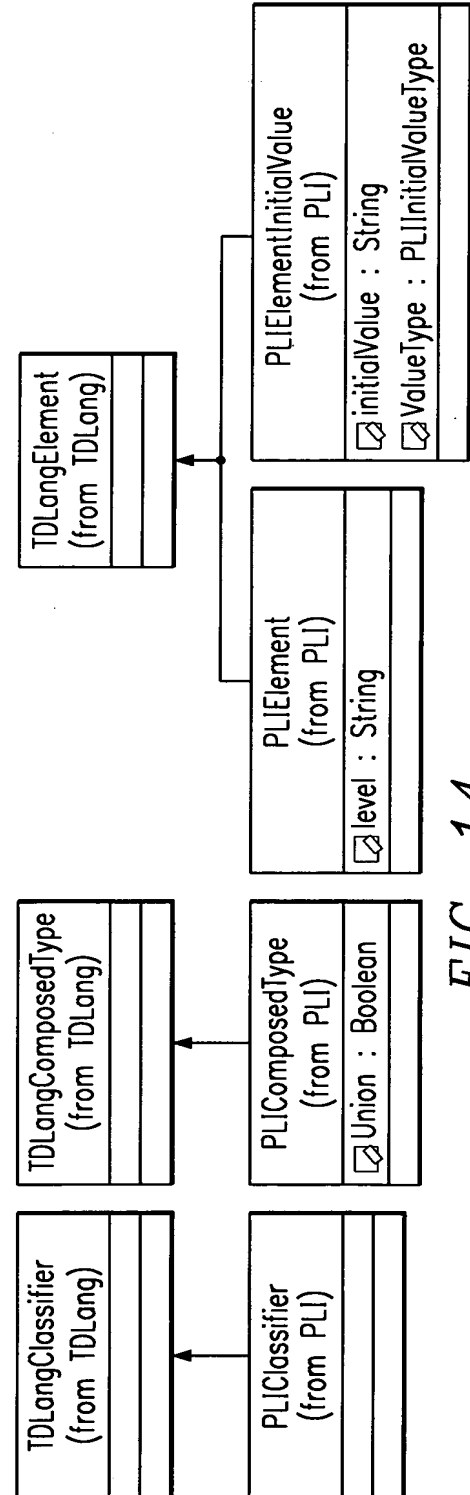


FIG. 14

6/14

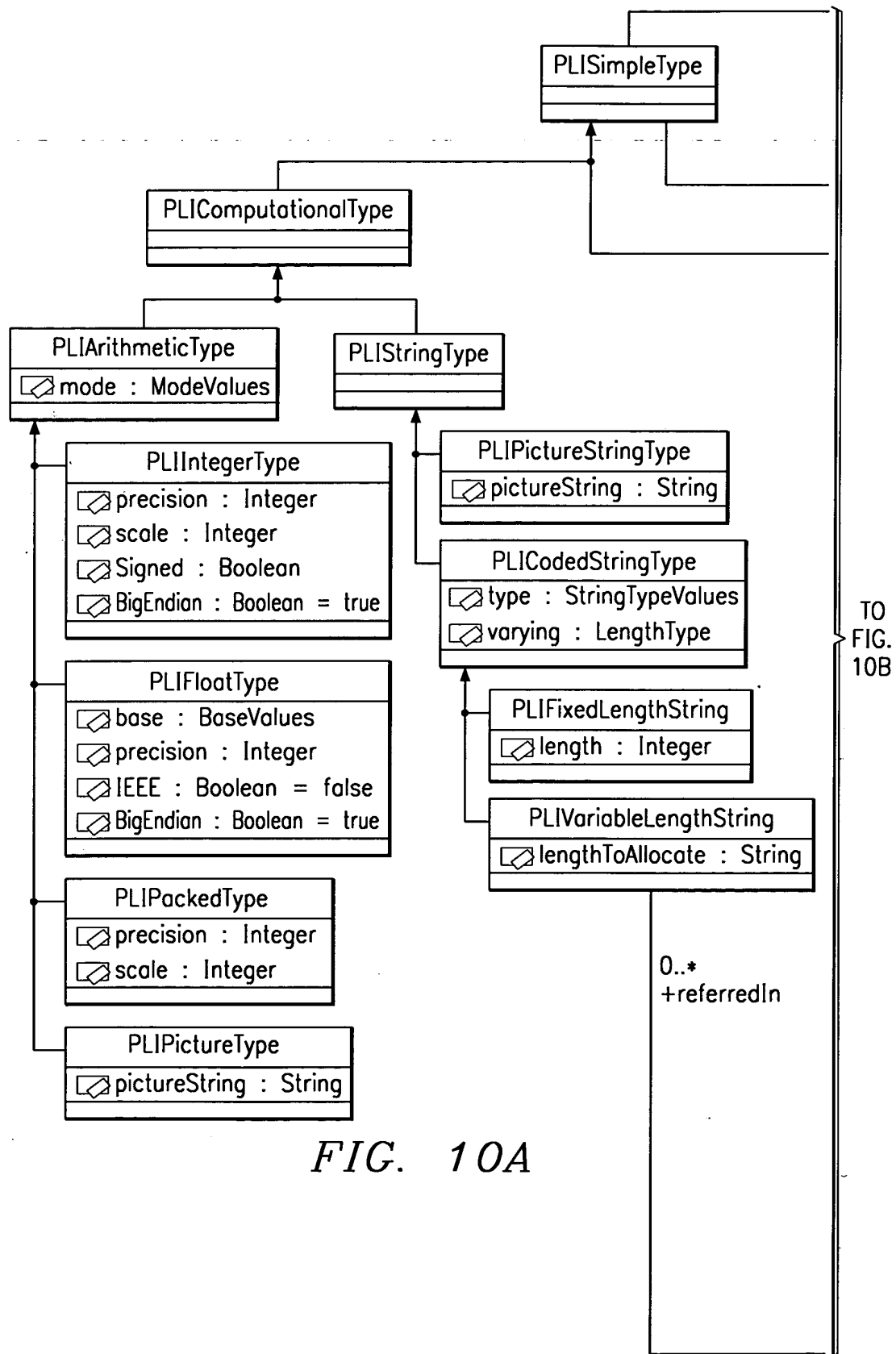
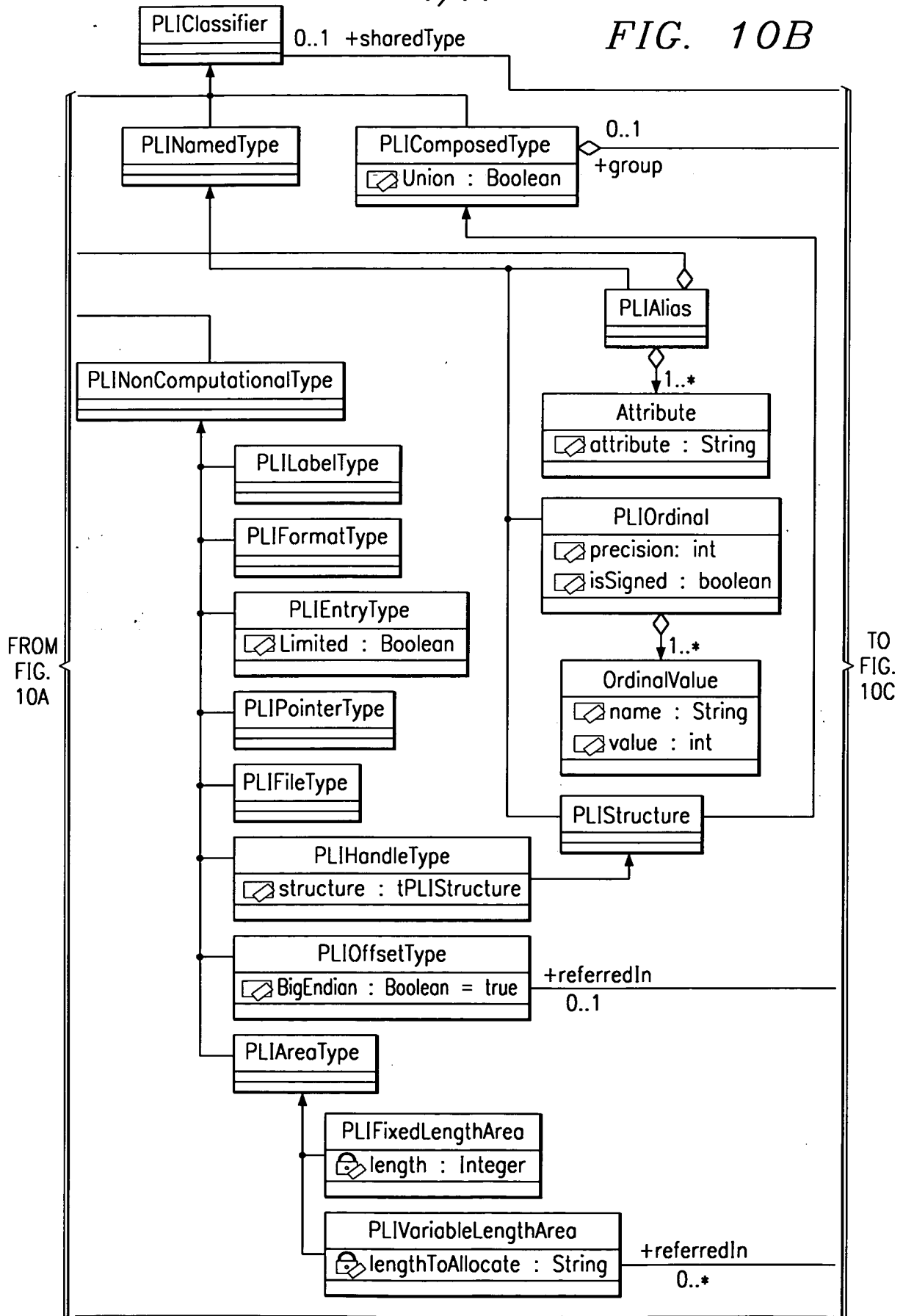


FIG. 10A

7/14

FIG. 10B



8/14

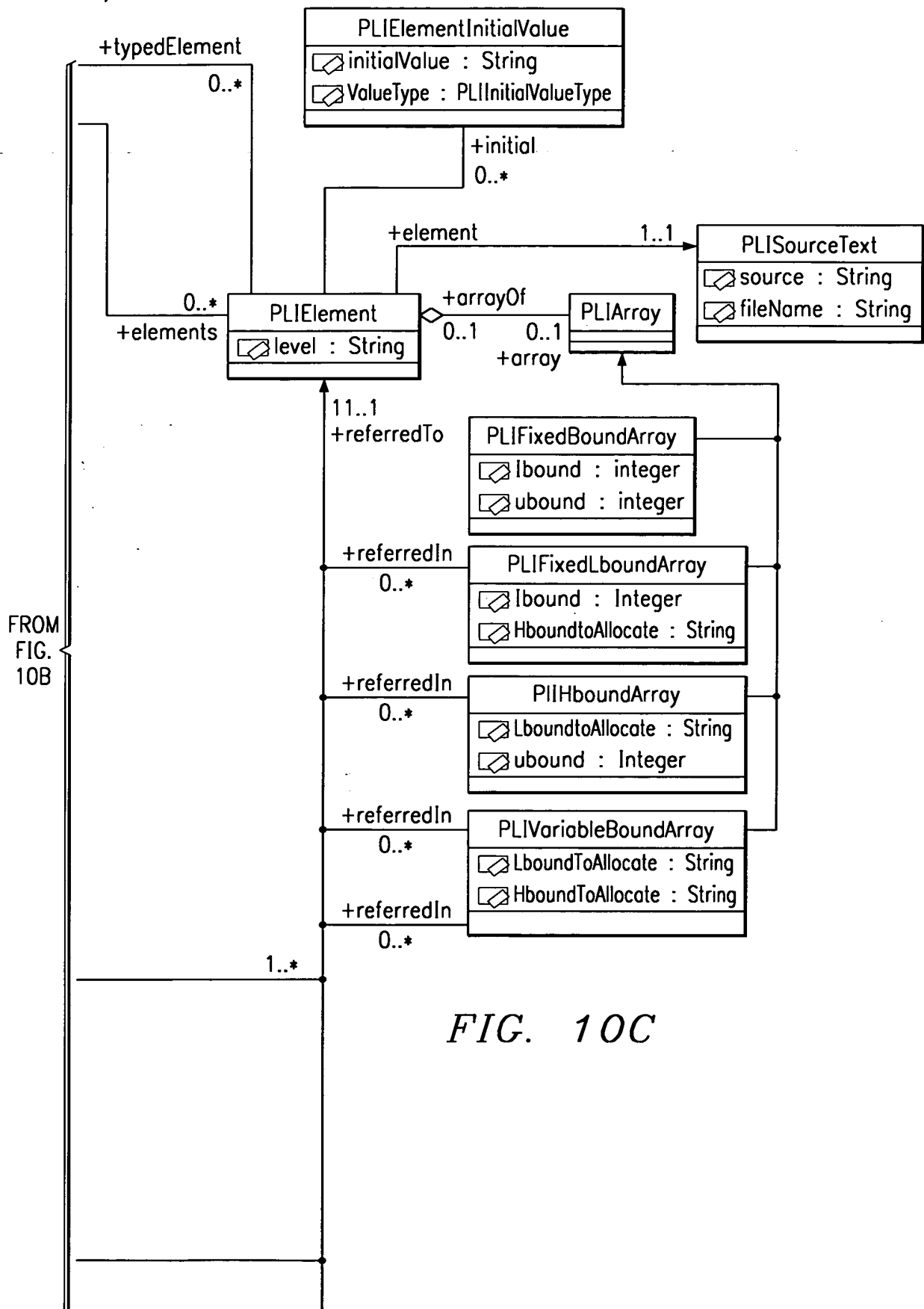


FIG. 10C

9/14

FIG. 11

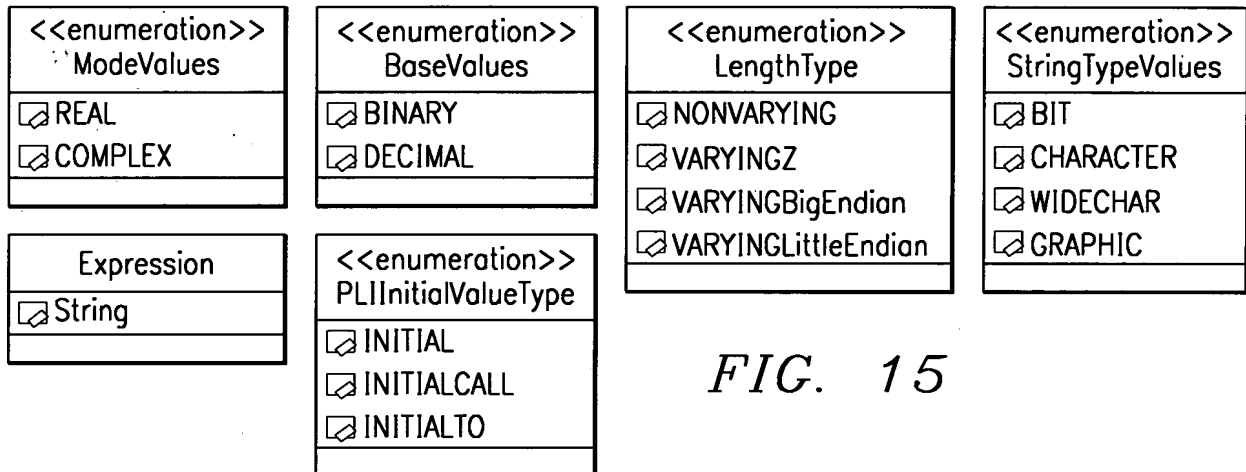
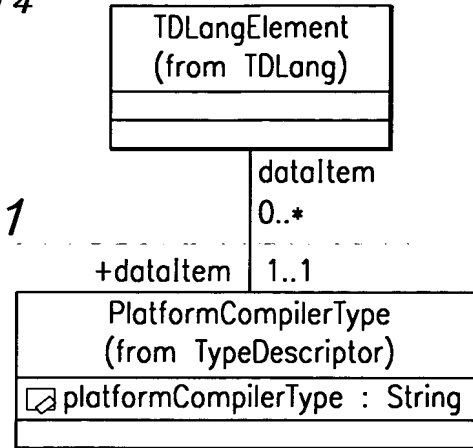


FIG. 15

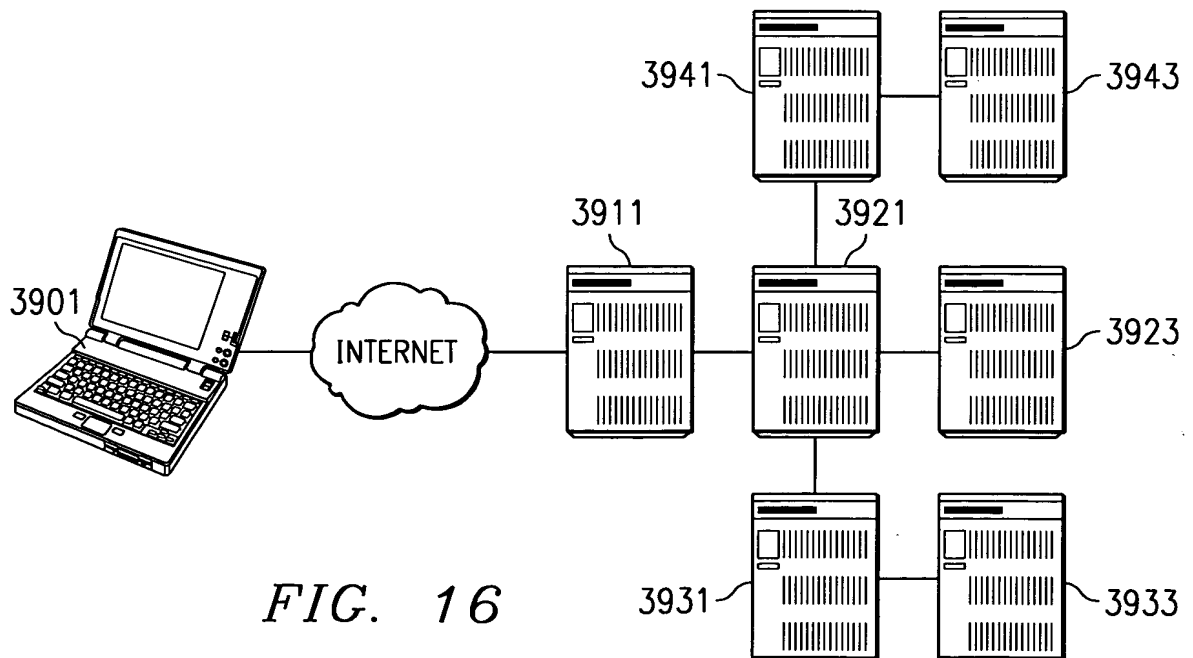


FIG. 16

10/14

<<enumeration>> signCoding	<input checked="" type="checkbox"/> twosComplement <input checked="" type="checkbox"/> onesComplement <input checked="" type="checkbox"/> signMagnitude <input checked="" type="checkbox"/> zoneSigns <input checked="" type="checkbox"/> packedSigns <input checked="" type="checkbox"/> unsignedBinary <input checked="" type="checkbox"/> unsignedDecimal
<<enumeration>> lengthEncoding	<input checked="" type="checkbox"/> fixedLength <input checked="" type="checkbox"/> lengthPrefixed <input checked="" type="checkbox"/> nullTerminated
<<enumeration>> floatType	<input checked="" type="checkbox"/> Unspecified <input checked="" type="checkbox"/> IEEEExtendedIntel <input checked="" type="checkbox"/> IEEEExtendedAIX <input checked="" type="checkbox"/> IEEEExtendedOS390 <input checked="" type="checkbox"/> IEEEExtendedAS400 <input checked="" type="checkbox"/> IEEEnonextended <input checked="" type="checkbox"/> IBM390Hex <input checked="" type="checkbox"/> IBM400Hex
<<enumeration>> accessor	<input checked="" type="checkbox"/> ReadOnly <input checked="" type="checkbox"/> WriteOnly <input checked="" type="checkbox"/> ReadWrite <input checked="" type="checkbox"/> NoAccess
<<enumeration>> packedDecimalSign	<input checked="" type="checkbox"/> MVS <input checked="" type="checkbox"/> MVSCustom <input checked="" type="checkbox"/> NT_OS2_AIX
<<enumeration>> bitModePad	<input checked="" type="checkbox"/> mode16 <input checked="" type="checkbox"/> mode24 <input checked="" type="checkbox"/> mode31 <input checked="" type="checkbox"/> mode32 <input checked="" type="checkbox"/> mode64 <input checked="" type="checkbox"/> mode128
<<enumeration>> addrUnit	<input checked="" type="checkbox"/> bit <input checked="" type="checkbox"/> byte <input checked="" type="checkbox"/> word <input checked="" type="checkbox"/> doubleWord

FIG. 12

FIG. 12

11/14

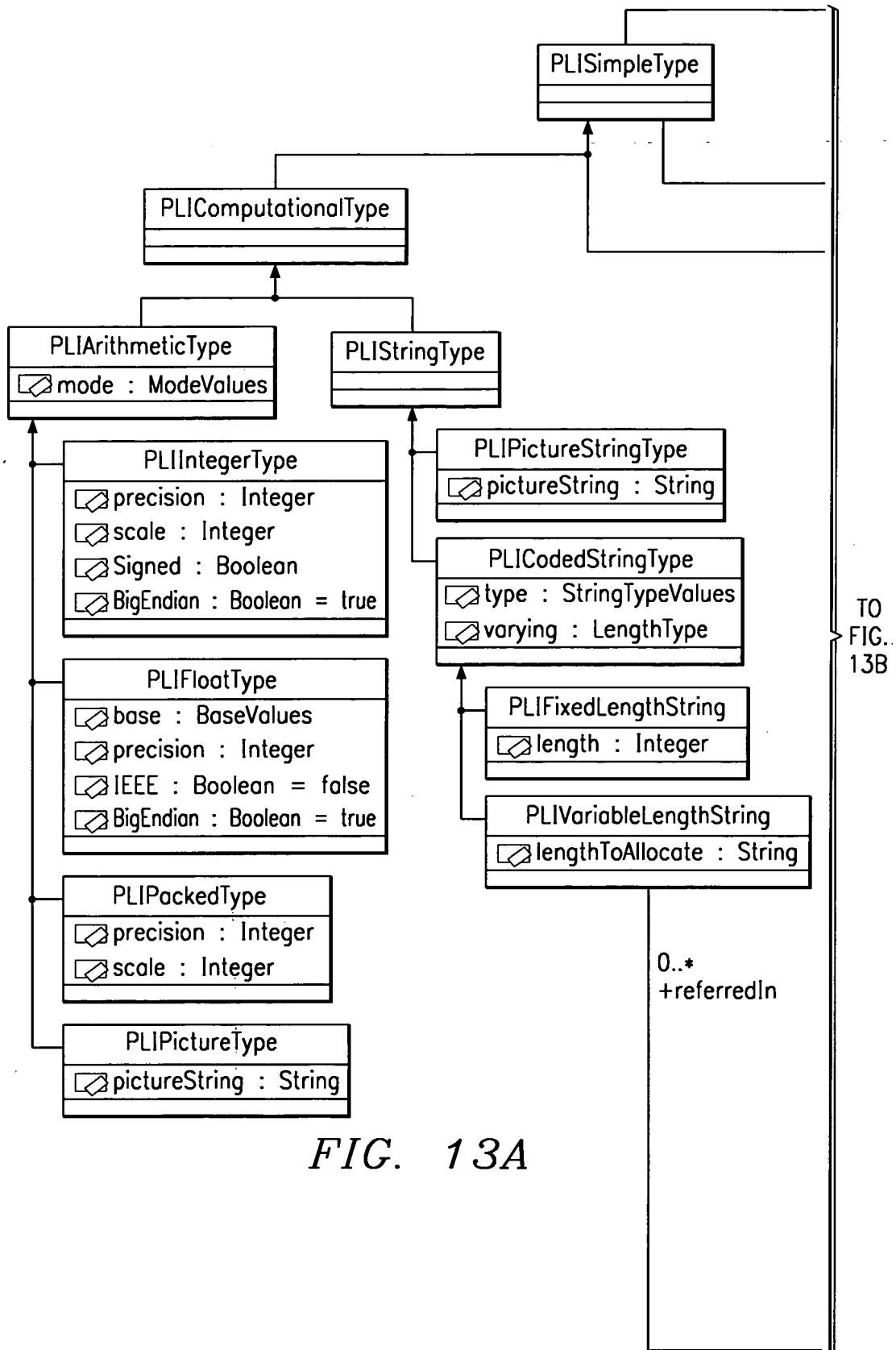
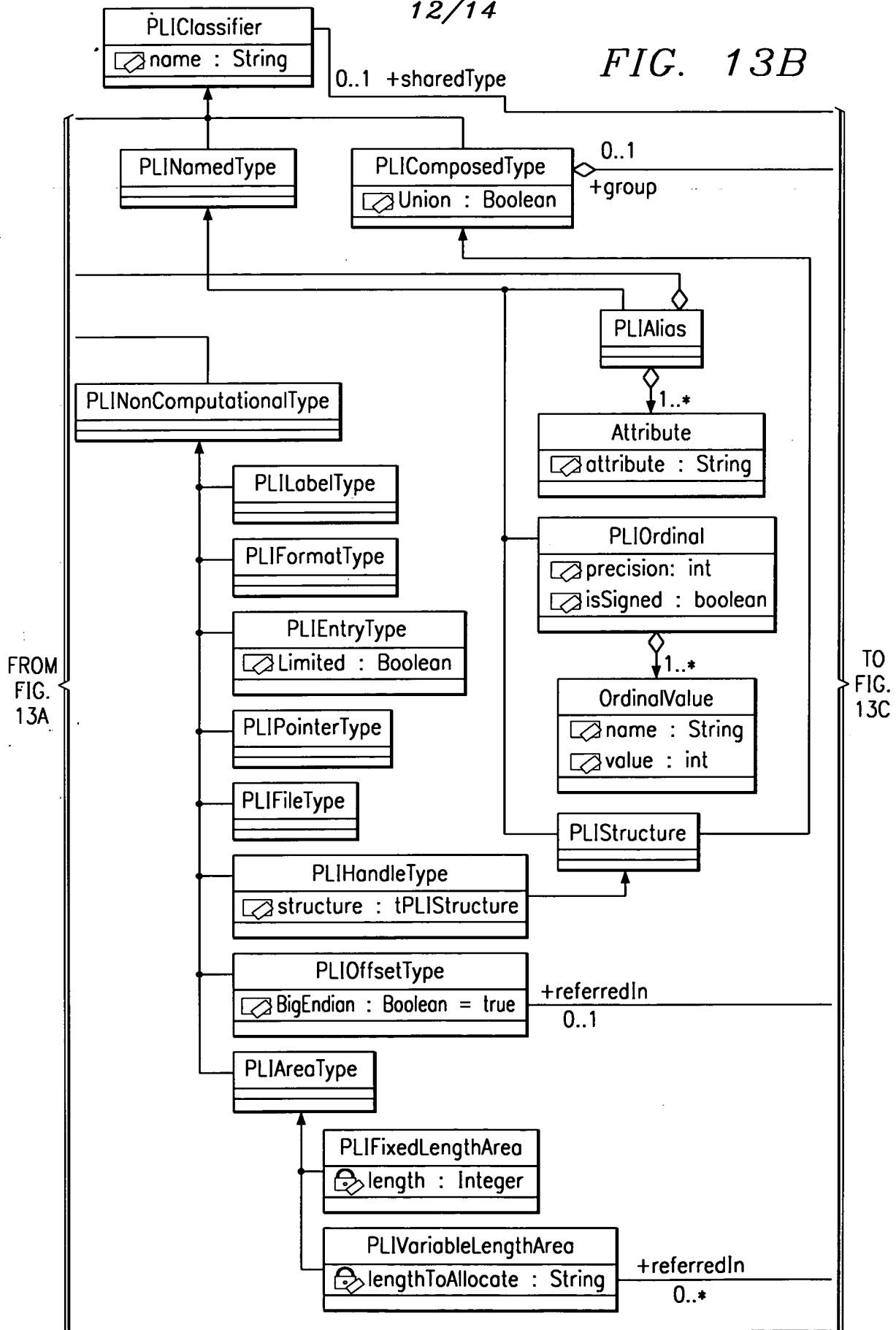


FIG. 13A

12/14

FIG. 13B



13/14

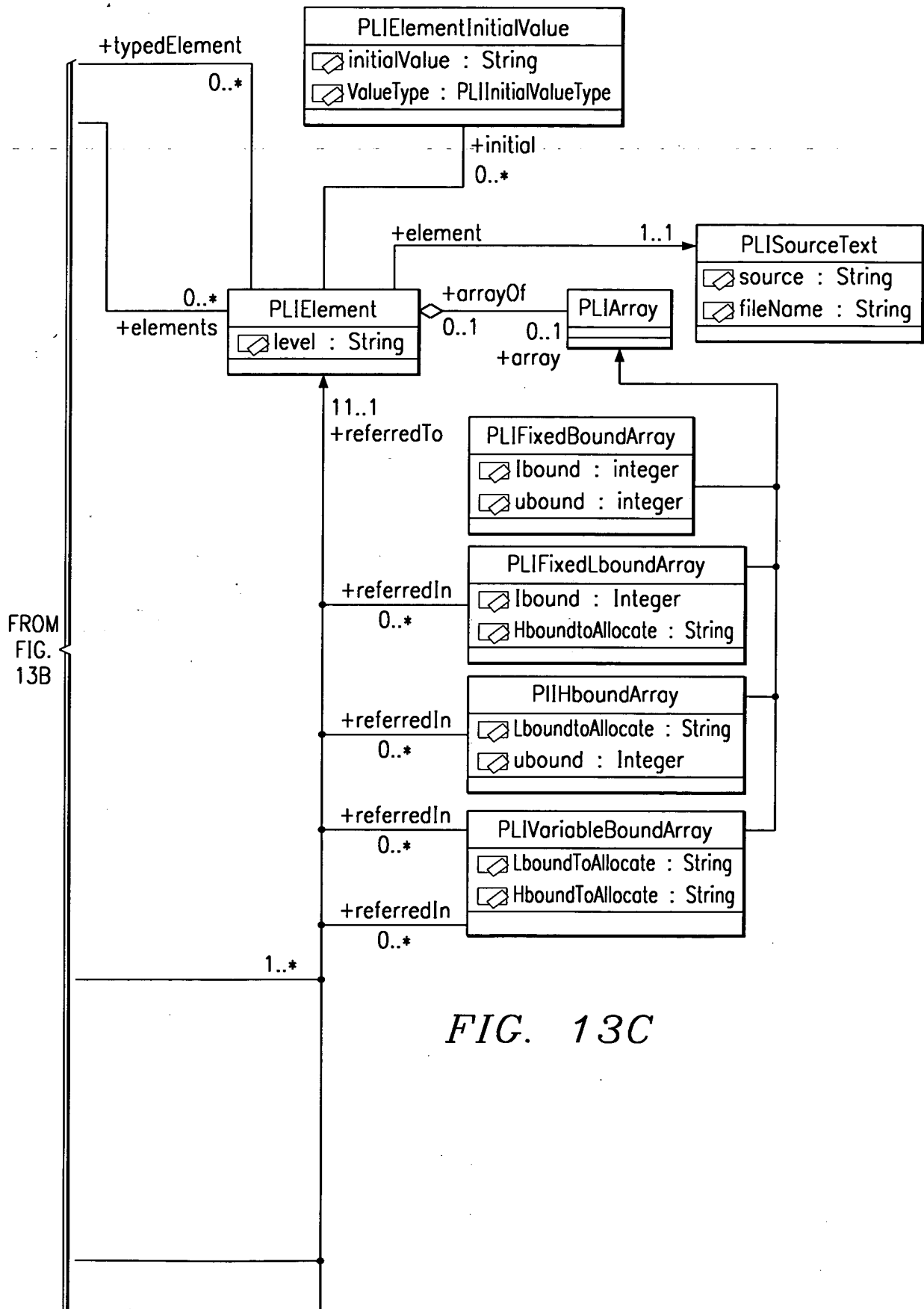


FIG. 13C

14/14

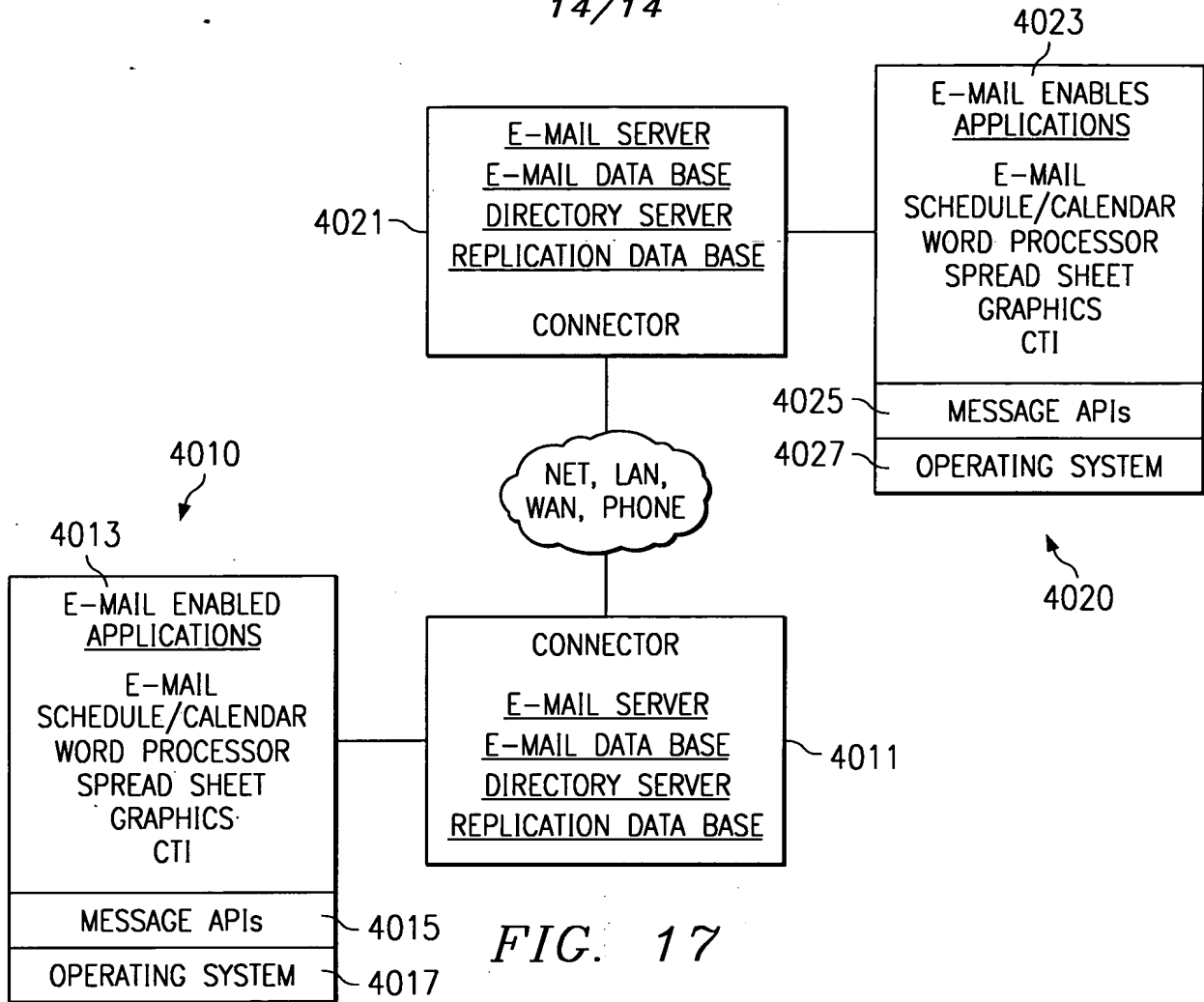


FIG. 17

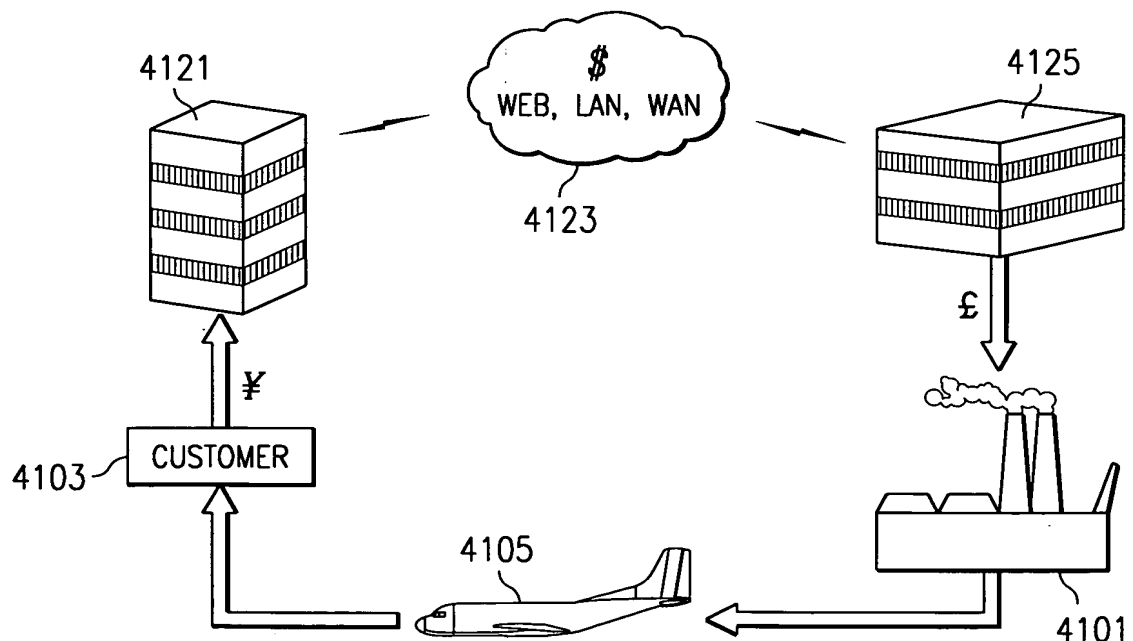


FIG. 18